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Fig. 1 PRIOR ART

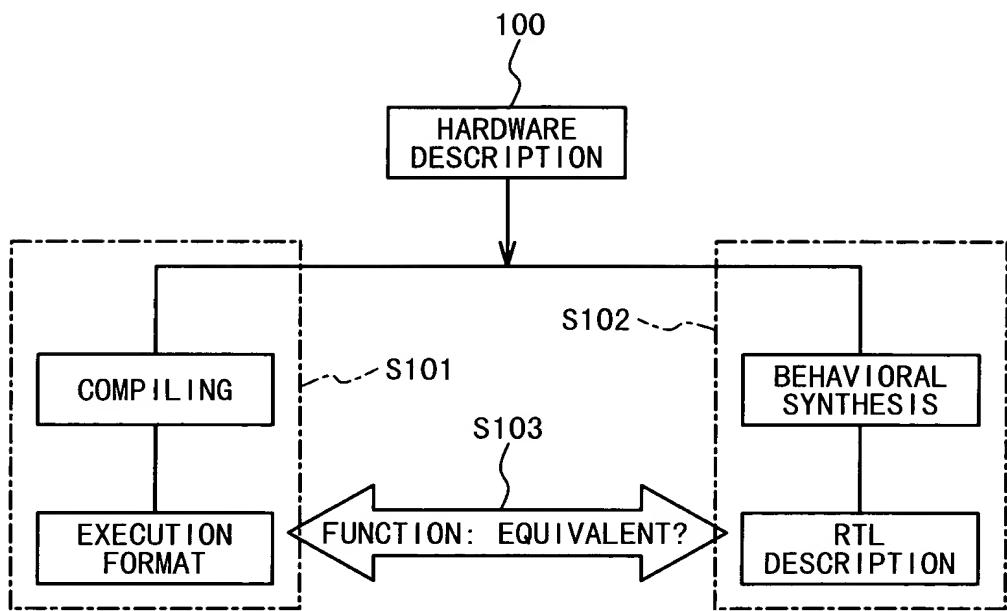


Fig. 2

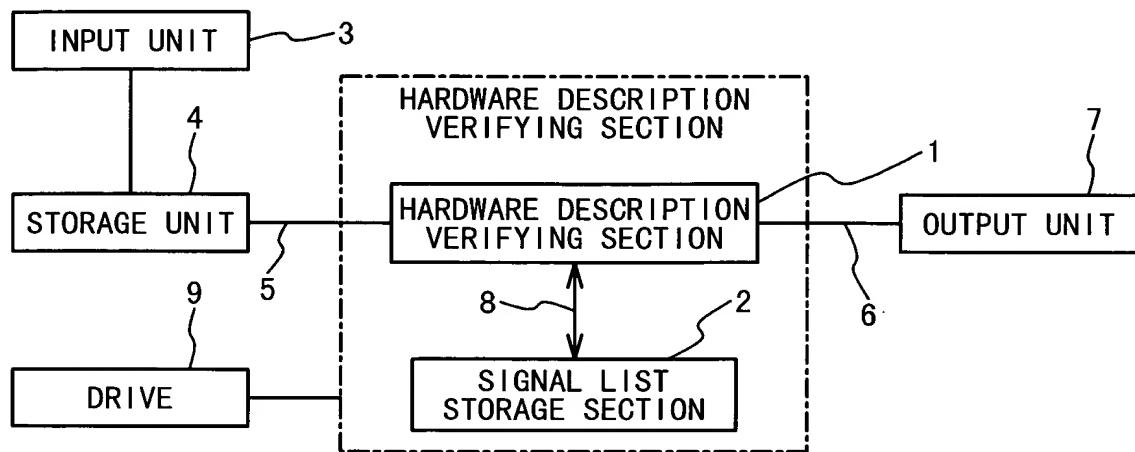


Fig. 3

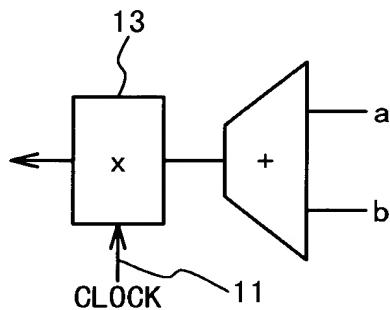


Fig. 4

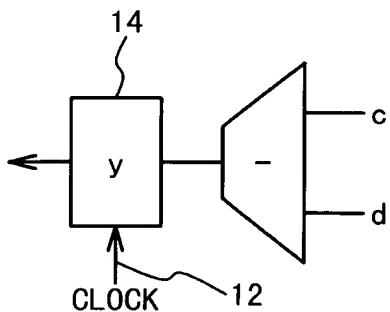


Fig. 5

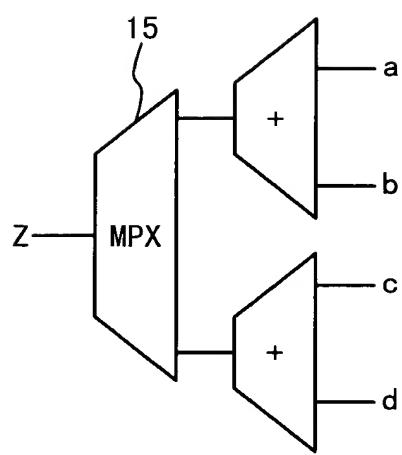


Fig. 6

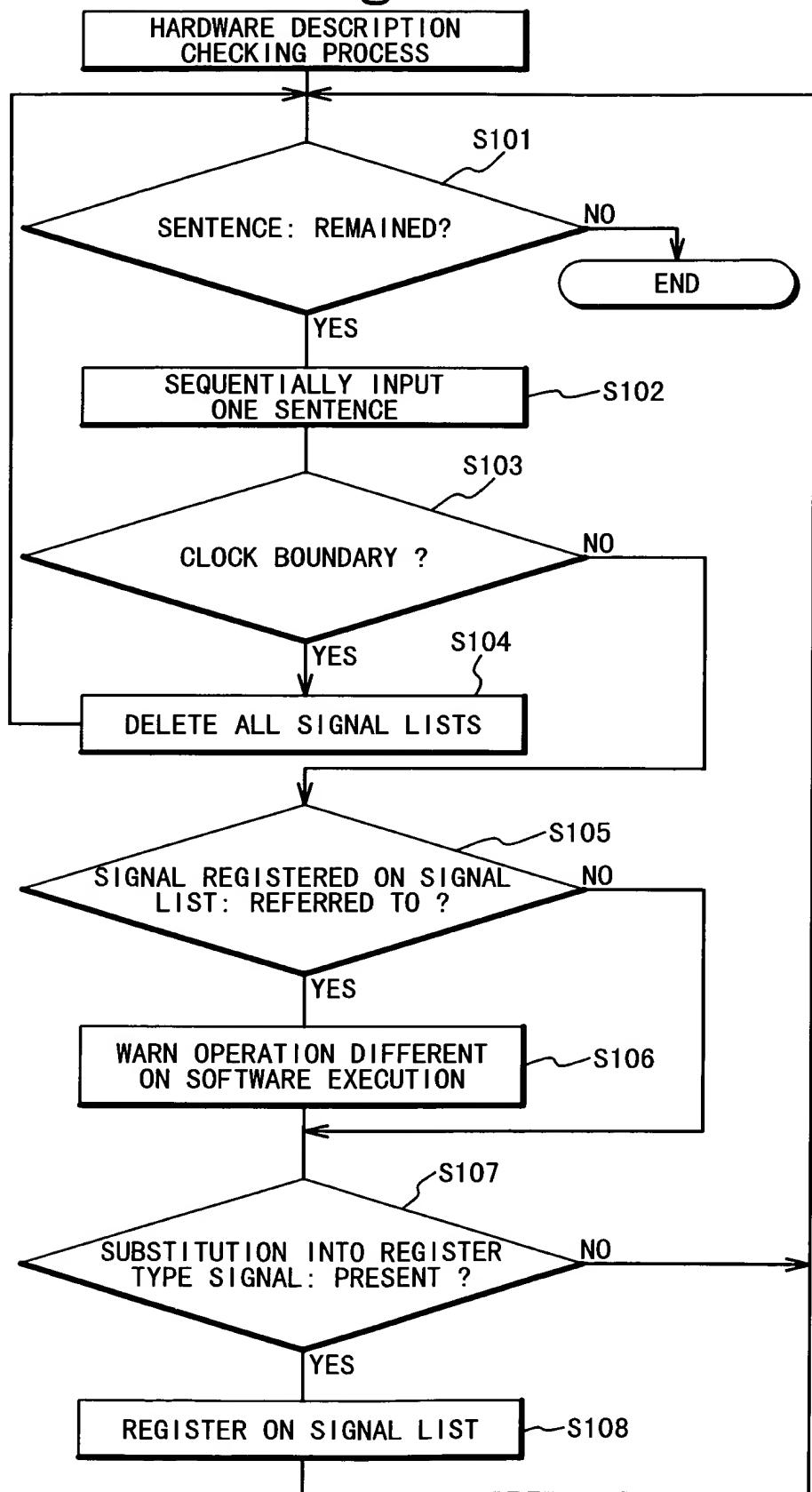


Fig. 7

```
/* REG.C */  
  
#IFDEF C  
# DEFINE REG UNSIGNED INT  
#ENDIF  
  
MAIN()  
/* C */  
/* HDL */  
/* LIST */  
  
INT T;  
REG x, y;  
  
x = 0; /* x=0 */  
CLOCK(); /* x=0 */  
x = 1; /* x=1 */  
t = 3; /* t=3 */  
y = x + t; /* y=4 */  
CLOCK(); /* x=1, Y=3 */  
  
/* x=0 */  
/* {} */  
/* [x] */  
/* [x] */  
/* [x] */  
/* [x, y] */  
/* {} */  
/* S107 */  
/* S103 */  
/* S107 */  
/* S105, S107 */  
/* S103 */
```

Fig. 8

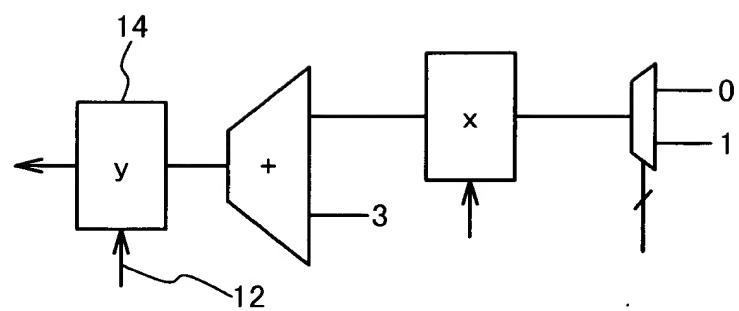
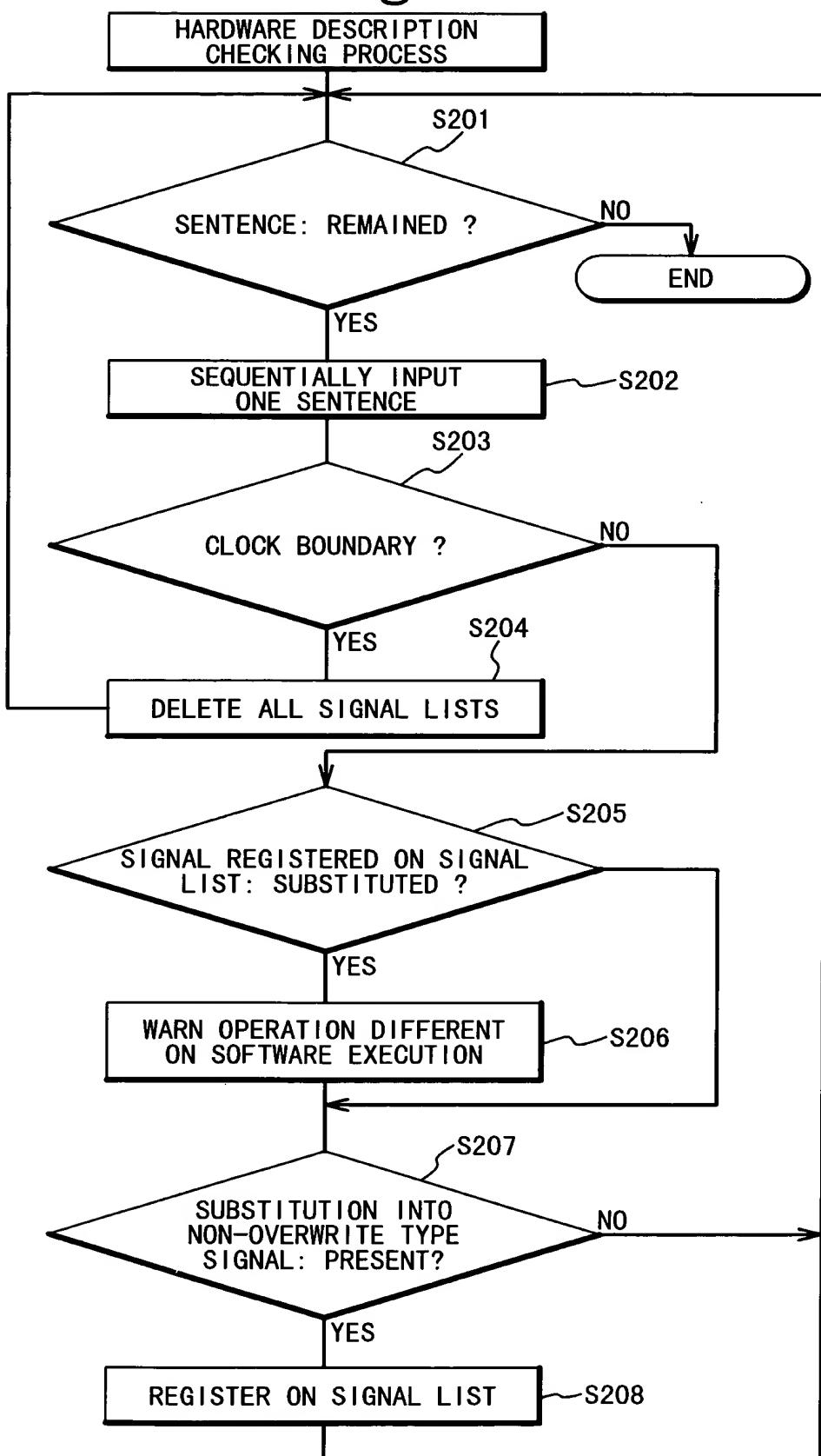


Fig. 9



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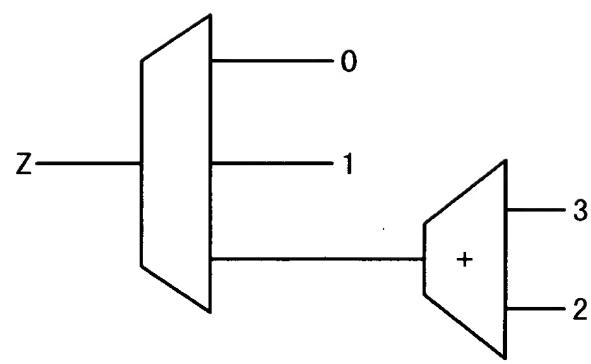
/* ASSIGN.C */

#ifndef C
#define TER UNSIGNED INT
#endif

MAIN()
{
    TER z, t;
    /* C */
    /* HDL */
    /* LIST */
    z = 0;
    /* z=0 */
    CLOCK();
    /* z=1 */
    z = 1;
    /* z=? */
    /* t=3 */
    t = 3;
    /* t=3 */
    /* z=5 */
    z = t + 2;
    /* z=? */
    CLOCK();
    /* z=0 */
    /* z=1 */
    /* t=3 */
    /* z=? */
    /* z=5 */
    /* z=? */
    /* z=0 */
    /* z=1 */
    /* t=3 */
    /* z=? */
    /* z=5 */
    /* z=? */
    /* z=0 */
    /* z=1 */
    /* t=3 */
    /* z=? */
    /* z=5 */
    /* z=? */
    /* z=0 */
    /* z=1 */
    /* t=3 */
    /* z=? */
    /* z=5 */
    /* z=? */
    /* z=0 */
    /* z=1 */
    /* t=3 */
    /* z=? */
    /* z=5 */
    /* z=? */
}

```

Fig. 11



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```

/* TER.C */

#ifndef C
#define TER UNSINED INT
#endif

MAIN()
{
    TER z, t;
    /* C */
    /* HDL */
    /* LIST */
    t = 3;
    /* t=3 */
    /* t=3 */
    CLOCK();
    /* z=5 */
    /* z=? */
    z = t + 2;
    /* z=? */
    /* z=? */
    CLOCK();
    /* z=? */
    /* z=? */
}

```

Fig. 13

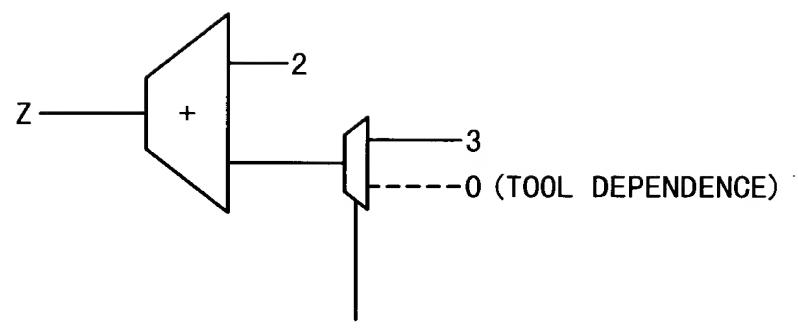
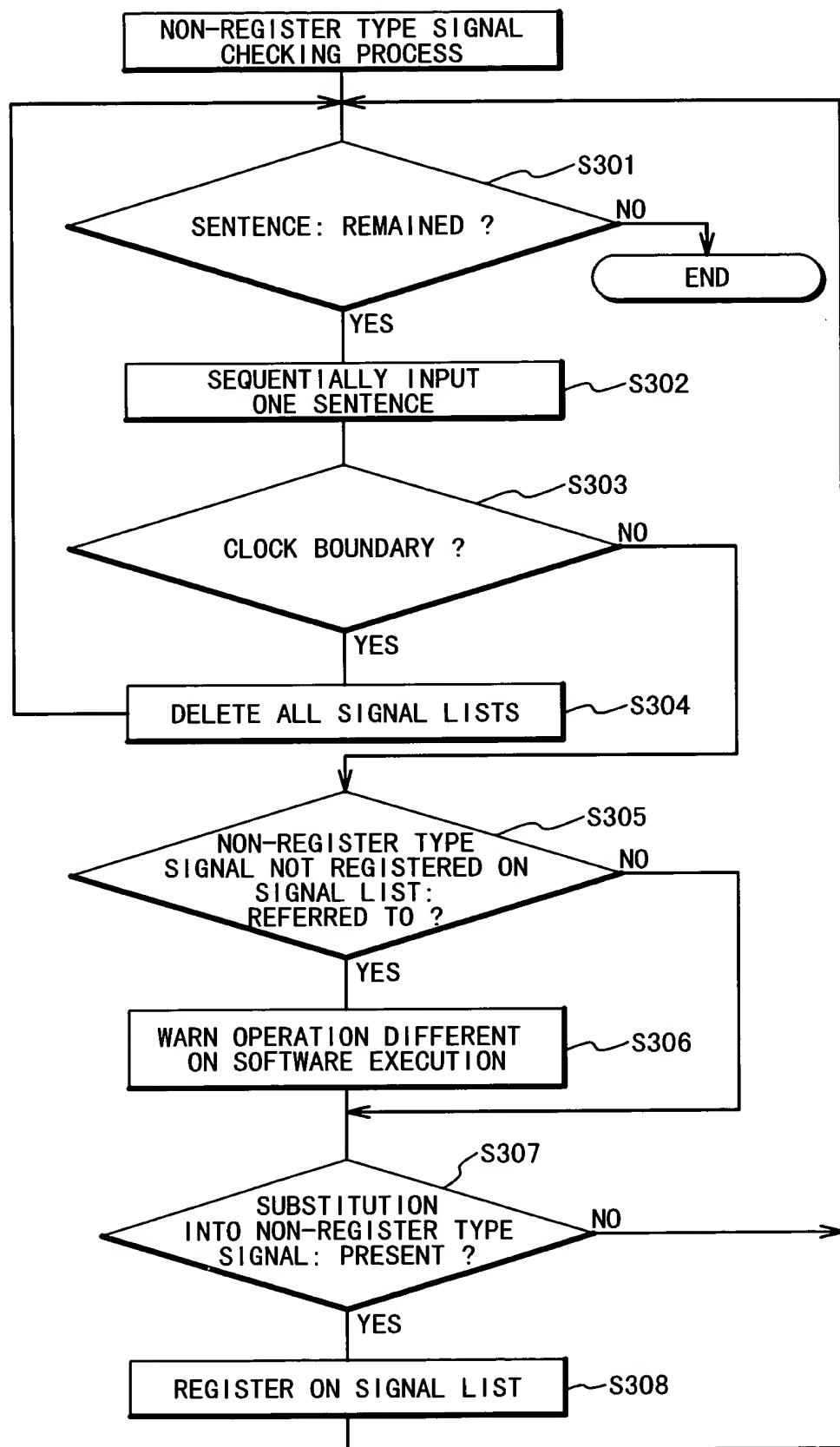


Fig. 14



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Fig. 16

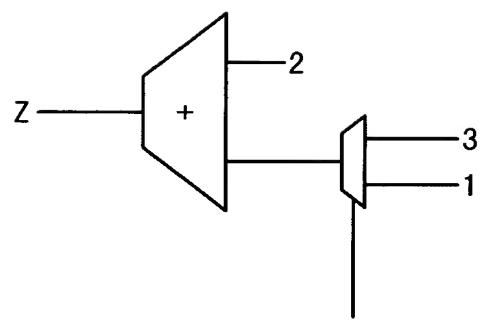


Fig. 17

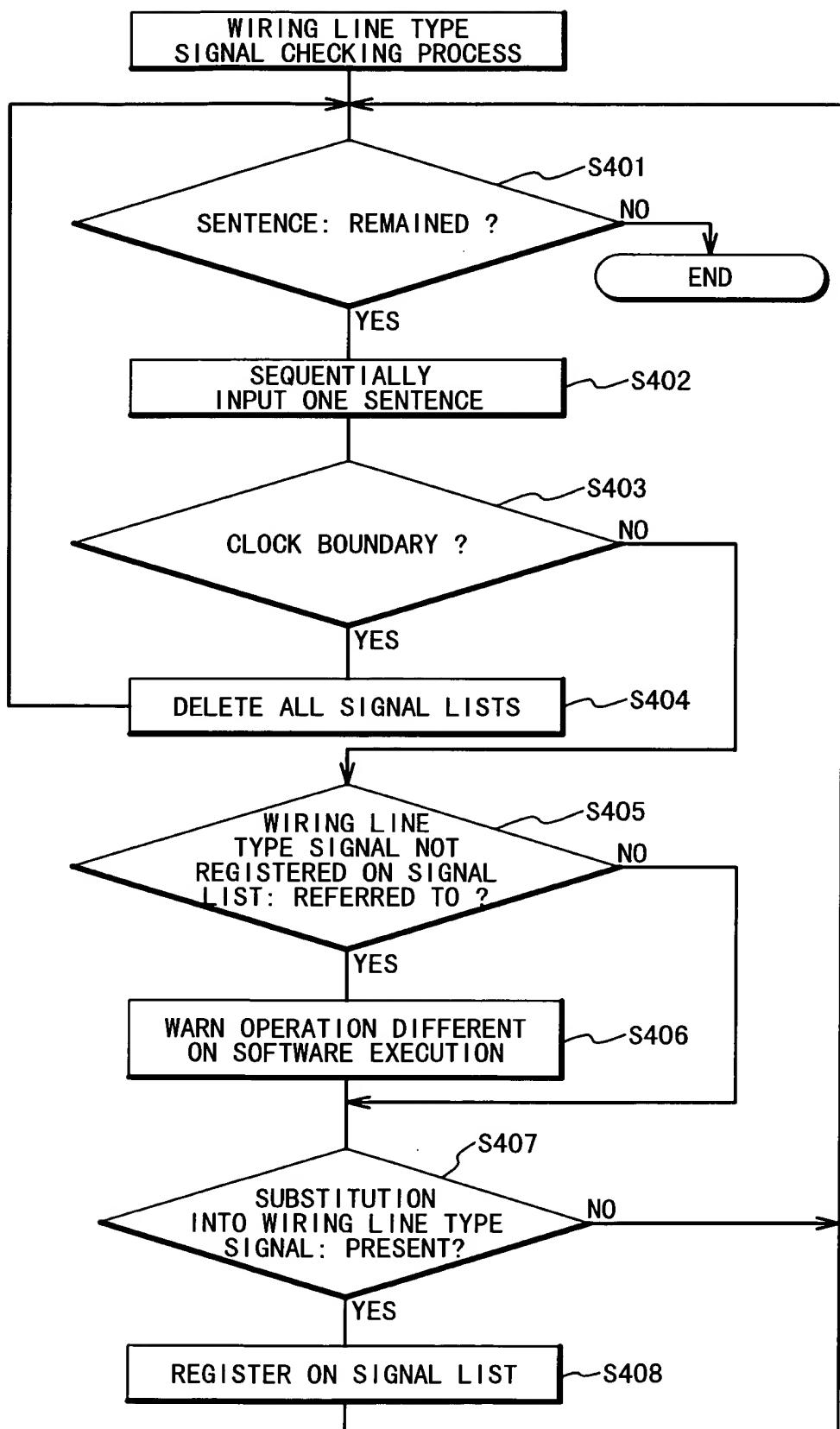


Fig. 18

```
/* AND.C */  
  
MAIN()  
{  
    INT a, i;  
  
    /* C */  
    /* HDL */  
  
    i = 0;          /* i=0 */  
    a = 0;          /* a=0 */  
    CLOCK();  
    if(i >0 && a++) {  
        /* a=0 */  
        /* a=1 */  
        i = 0;  
    }  
    CLOCK();
```

Fig. 19

